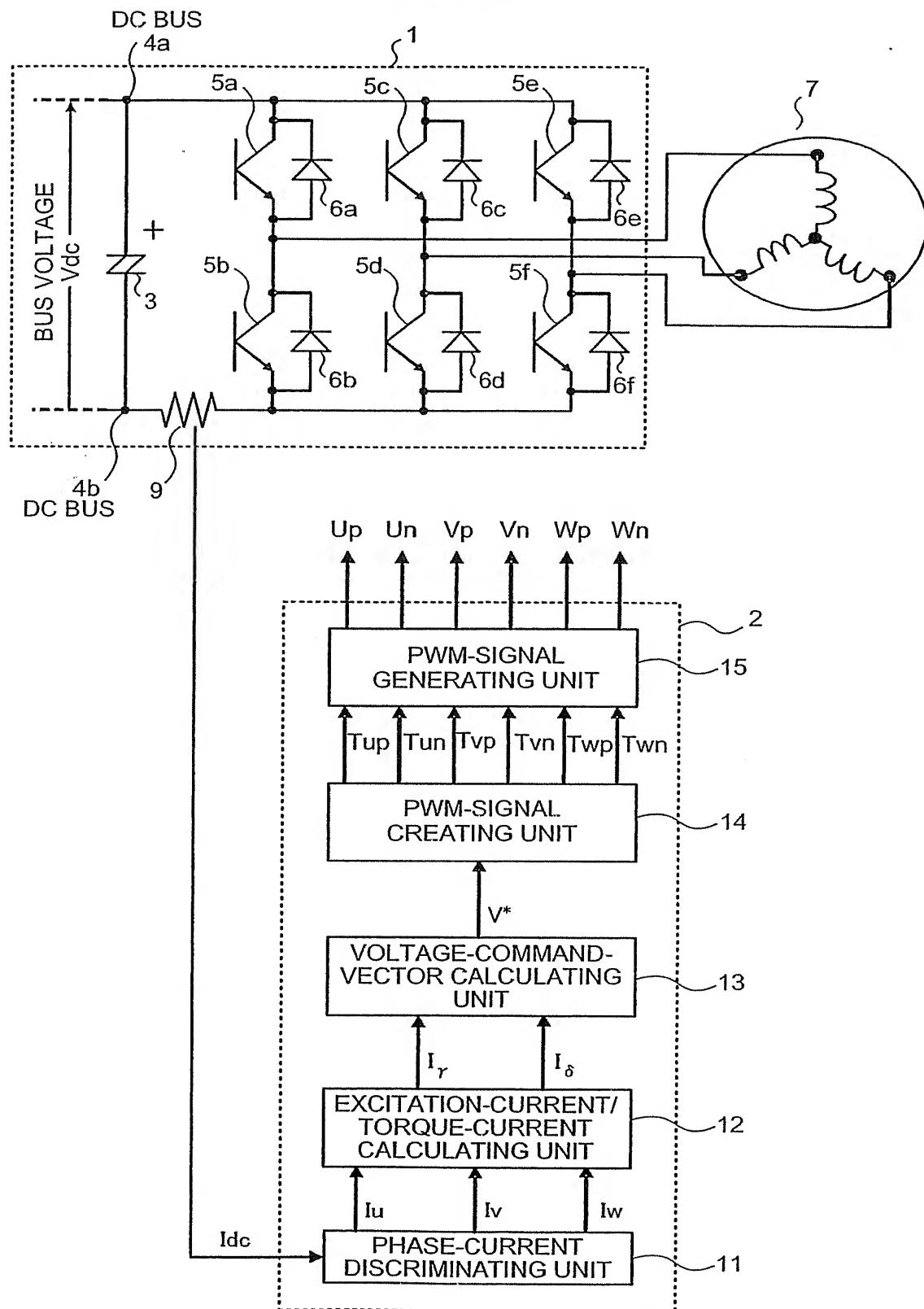


**CONVENTIONAL**

**FIG.36**



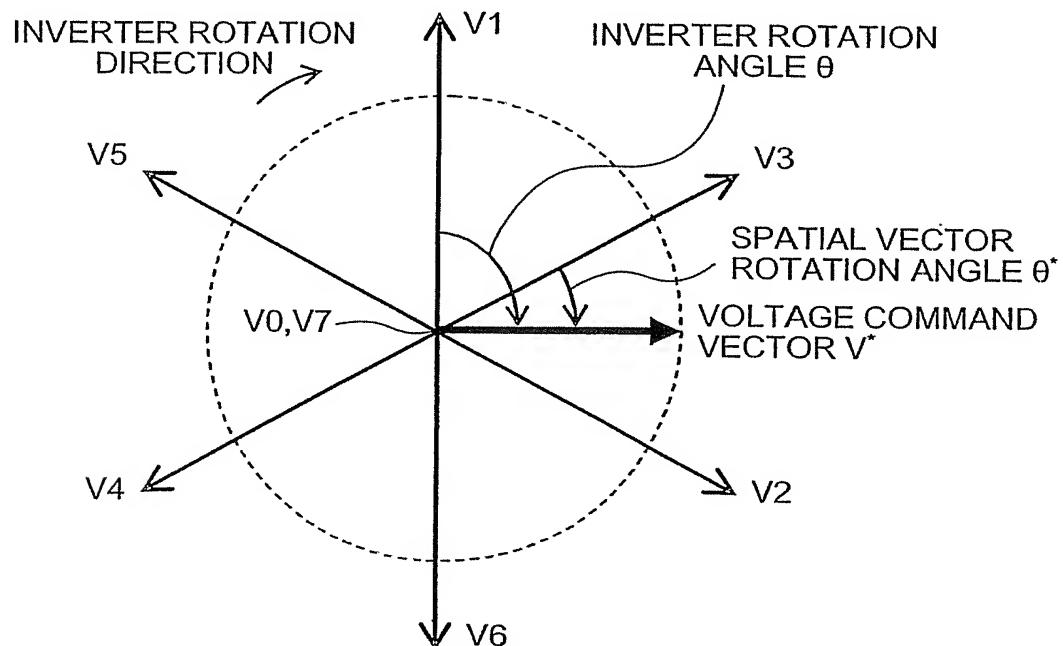
CONVENTIONAL

**FIG.37**

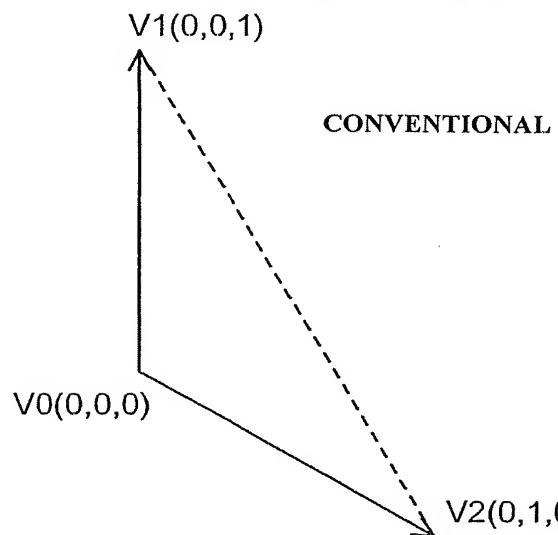
BASIC VOLTAGE VECTOR	W <sub>p</sub> (W-PHASE POSITIVE SIDE SWITCHING ELEMENT LOGICAL STATE)	V <sub>p</sub> (V-PHASE POSITIVE SIDE SWITCHING ELEMENT LOGICAL STATE)	U <sub>p</sub> (U-PHASE POSITIVE SIDE SWITCHING ELEMENT LOGICAL STATE)	OBSERVABLE PHASE CURRENT
V0	0	0	0	OBSERVATION IMPOSSIBLE
V1	0	0	1	I <sub>u</sub>
V2	0	1	0	I <sub>v</sub>
V3	0	1	1	-I <sub>w</sub>
V4	1	0	0	I <sub>w</sub>
V5	1	0	1	-I <sub>v</sub>
V6	1	1	0	-I <sub>u</sub>
V7	1	1	1	OBSERVATION IMPOSSIBLE

CONVENTIONAL

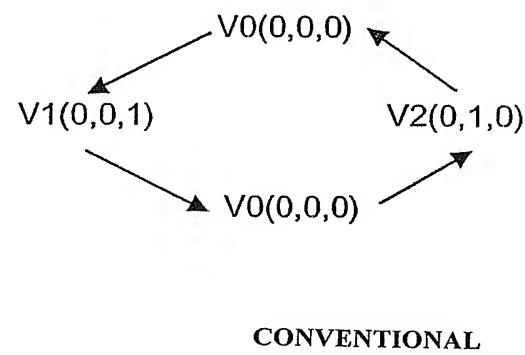
**FIG.38**



**FIG.39A**

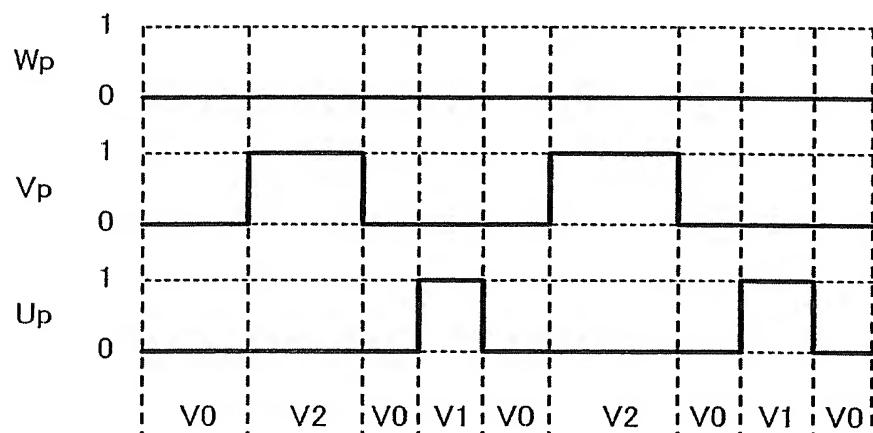


**FIG.39B**

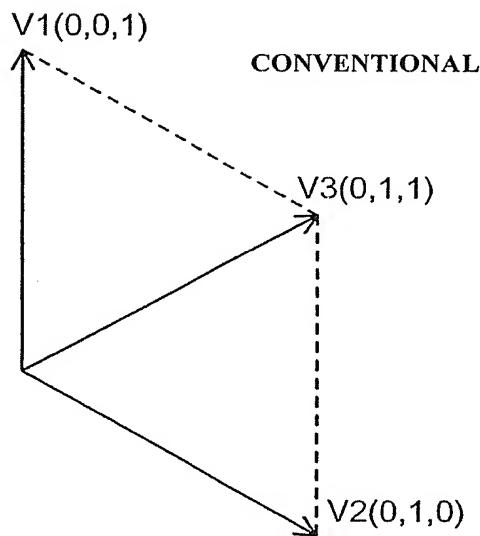


**CONVENTIONAL**

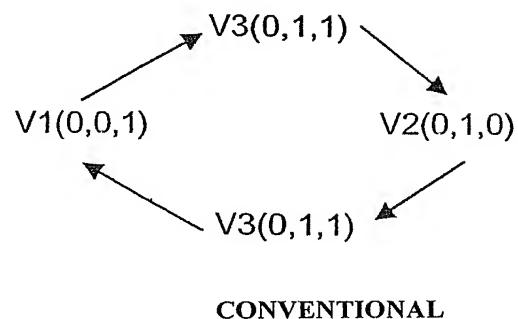
**FIG.40**



**FIG.41A**



**FIG.41B**



**FIG.42**

CONVENTIONAL

